



PATENT  
81754.0061

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Noriyuki SARUHASHI ,et al.

Serial No: 09/871,596

Confirmation No.: 8586

Filed: May 30, 2001

For: Physical Layer Device Testing  
Method, Physical Layer Device  
With Test Circuits, and  
Transmitting/Receiving Circuit  
With Test Circuits

Art Unit: 2133

Examiner: Torres, Joseph D.

I hereby certify that this correspondence  
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September 22, 2004

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Name

Signature

September 22, 2004

Date

PETITION FOR EXTENSION OF TIME

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with 37 C.F.R. 1.136, Applicant respectfully petitions the Commissioner for a three-month extension of time extending to September 24, 2004, the period for response to the Final Office Action dated March 24, 2004. A check for \$840 is enclosed in payment of the three month extension fee of \$950, minus the \$110 payment that was previously submitted on July 26, 2004, along with the petition for a one month extension of time. The responsive paper(s) are attached.

Please charge any insufficiency or credit any overpayment to Deposit Account No. 50-1314. A copy of this petition is enclosed.

09/28/2004 AWONDAF1 00000057 09871596

02 FC:1253

840.00 OP

Respectfully submitted,

HOGAN & HARTSON L.L.P.

Date: September 22, 2004

By: 

Troy M. Schmelzer

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